

A n 32-bit data is stored starting from memory location **dat1**. Write an ALP that will convert every m^{th} double word from little endian to big endian format (only the m^{th} double word should be converted). The value of m can range between 2 to 4 and is stored in location **off1**. The value of n is between 10_d to 120_d and is stored in location **cnt1**. [*The size n need not be a multiple of m*]