Date: 08-04-2016   **Max Marks: 10**   **Duration: 30 Mins**

ID No.   **Name:**

Q1. The base address of an 8259 master is $A0_H$. The base address of the 8259 slave is $B0_H$ The code snippet used for initializing 8259 master & slave is as follows

```
MOV    AL, 00010001_B
OUT    0A0_H, AL
MOV    AL, 80_H
OUT    0A2_H, AL
MOV    AL, 04_H
OUT    0A2, AL
MOV    AL,01_H
OUT    0A2_H, AL
MOV    AL, 00010001_B
OUT    0B0_H, AL
MOV    AL, 88_H
OUT    0B2_H, AL
MOV    AL, 02_H
OUT    0B2, AL
MOV    AL,01_H
OUT    0B2_H, AL
```

Answer the following questions:

a) Has Automatic EOI been enabled for Master & Slave?   **[2]**

No for Both

b) Are the Interrupts Master & Slave Level Triggered or Edge Triggered?   **[2]**
Edge triggered for both Master & Slave

c) To what Interrupt Request of the Master is the Slave connected?   **[2]**
IR2

d) What is the starting vector number allocated to Master?   **[2]**

80_H

e) What is the starting vector number allocated to Slave?   **[2]**
88_H