

Birla Institute of Technology & Science, Pilani- KK Birla Goa Campus
II Semester 2015-2016
EEE/CS/INSTR F241- MICROPROCESSOR PROGRAMMING AND INTERFACING
Quiz 3 (Open Book)

Date: 11-03-2016

Max Marks: 10

Duration: 30 Mins

ID No.

Name:

Instructions: Answer the questions in the space provide

- Q1.** If the starting address of a read and write valid Non-system data segment in 80386 is 0A 10 00 00_H and its size is 4GB, Privilege Level is 01, has not been accessed before and addresses have to be incremented to read consecutive data(i.e. address expands upwards), what will be the 8-byte descriptor? The segment operation is in 32-bit mode and is available in memory. **(Write from MSB onwards)** **[7]**

_____ 0A DF B2 10 00 00 FF FF _____

0A 10 00 00 – in proper position 2 M Access Right Byte B2 – 2M, The granularity nibble 1 M, Size – FF FF 2M

- Q2.** If Paging is enabled and there are 1024x1024 Pages what will be size of memory required for storing the

paging tables _____ 4MB _____

paging directory _____ 4KB _____

[2+1]